

Attorney Docket: 040021-0305238  
Client Reference: OPP 030839 US



JW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of: PARK

Confirmation Number: 9322

Application No.: 10/626,549

Group Art Unit: 2811

Filed: July 25, 2003

Examiner: Unassigned

Title: METHOD FOR MANUFACTURING MOS TRANSISTOR AND SEMICONDUCTOR DEVICE EMPLOYING MOS TRANSISTOR MADE USING THE SAME

May 20, 2004

PRELIMINARY AMENDMENT

Mail Stop Non-Fee Amendments  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Omitted Items, dated March 23, 2004, and prior to initial examination, please amend the above-identified application as follows: